In the Specification

Amend the specification as follows:

Amend paragraph 0041 as follows:

[0041]   Fig. 1 depicts the overall process flow for the present invention. The invention input file 6 is a software tool that provides a graphical user representation for a printed circuit board design file 1 and the output of a design rule checker 4. The overall application of the invention is typically as follows: The user applies rule checker parameters 3 against a design file) or some subset of the design file data 2 within a design rule checker 4. The rule checker 4 compares the design data 1, 2 against the rule checker parameters 3 and provides output to the user in text form 5 and in a standard file format specified for the input file 6. The present invention then employs a "violation viewer" methodology 8 that generates an output file 9 allowing the user to save information related to identified violations. This includes the data from the design file 1, and uses the input file 6 to graphically identify the rule checker output to the user.